

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	12	((FRANK) near2 (LECLERG)).INV.	US-PGPUB; USPAT	OR	OFF	2007/03/01 15:07
S3	35	((PETER) near2 (VOGT)). INV.	US-PGPUB; USPAT	OR	OFF	2007/03/01 15:08
S4	46	S2 S3	US-PGPUB; USPAT	OR	OFF	2007/03/01 15:09
S6	3	S4 and (memory NEAR2 initializ\$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/03/01 15:11
S8	3	S4 and (memory NEAR3 initializ\$6).clm.	US-PGPUB; USPAT	OR	ON	2007/03/01 15:12
S7	3	S4 and (memory NEAR3 initializ\$3).clm.	US-PGPUB; USPAT	OR	ON	2007/03/01 15:12
S9	3	S4 and (memory NEAR3 initializ\$6)	US-PGPUB; USPAT	OR	ON	2007/03/01 15:13
S10	3	S4 and (memory NEAR3 initializ\$5)	US-PGPUB; USPAT	OR	ON	2007/03/01 15:14
S14	15	(memory NEAR3 initializ \$5) with poll\$3	US-PGPUB; USPAT	OR	ON	2007/03/01 15:16
S13	1972	(memory NEAR3 initializ \$5) and poll\$3	US-PGPUB; USPAT	OR	ON	2007/03/01 15:16
S12	1	S4 and (memory NEAR3 initializ\$5) and poll\$3	US-PGPUB; USPAT	OR	ON	2007/03/01 15:16
S16	1312	"714"/\$.ccls. and (memory NEAR3 initializ\$5)	US-PGPUB; USPAT	OR	ON	2007/03/01 15:18
S15	2	"714"/\$.ccls. and (memory NEAR3 initializ\$5) with poll \$3	US-PGPUB; USPAT	OR	ON	2007/03/01 15:18
S17	152	"714"/\$.ccls. and (memory NEAR3 initializ\$5 near3 test \$3)	US-PGPUB; USPAT	OR	ON	2007/03/01 15:19
S18	13	"714"/\$.ccls. and (memory NEAR3 access\$3 with initializ\$5 near3 test\$3)	US-PGPUB; USPAT	OR	ON	2007/03/01 15:21
S21	27	(memory with bufer)	US-PGPUB; USPAT	OR	ON	2007/03/01 17:05
S22	794	memory adj module near3 buffer	US-PGPUB; USPAT	OR	ON	2007/03/01 17:06
S23	243	"711"/\$.ccls. and memory adj module near3 buffer	US-PGPUB; USPAT	OR	ON	2007/03/01 17:07
S25	175	"711"/\$.ccls. and memory adj access\$3 near1 buffer	US-PGPUB; USPAT	OR	ON	2007/03/01 17:08
S24	498	"711"/\$.ccls. and memory adj access\$3 near3 buffer	US-PGPUB; USPAT	OR	ON	2007/03/01 17:08

S26	11	"711"/\$.ccls. and memory adj access\$3 near2 buffer near3 bus	US-PGPUB; USPAT	OR	ON	2007/03/01 17:12
S27	41	memory adj access\$3 near2 buffer near3 bus	US-PGPUB; USPAT	OR	ON	2007/03/01 17:13
S28	1	memory adj access\$3 near2 buffer near3 initializ \$5	US-PGPUB; USPAT	OR	ON	2007/03/01 17:14
S29	102	memory adj buffer near3 initializ\$5	US-PGPUB; USPAT	OR	ON	2007/03/01 17:18
S31	105	memory adj controller near3 buffer near3 access \$3	US-PGPUB; USPAT	OR	ON	2007/03/01 17:24
S33	88	memory adj module near3 controller near3 buffer	US-PGPUB; USPAT	OR	ON	2007/03/01 17:26
S36	6	"714"/\$.ccls. and memory adj module near3 BIST near3 controller	US-PGPUB; USPAT	OR	ON	2007/03/01 17:29
S35	259	"714"/\$.ccls. and memory adj module near3 controller	US-PGPUB; USPAT	OR	ON	2007/03/01 17:29
S39	13	"714"/\$.ccls. and memory adj module with BIST near3 controller	US-PGPUB; USPAT	OR	ON	2007/03/01 17:30
S37	45	"714"/\$.ccls. and memory adj BIST near3 controller	US-PGPUB; USPAT	OR	ON	2007/03/01 17:30
S40	4	US-4236207-\$.DID. OR US- 6049476-\$.DID. OR US- 6092146-\$.DID. OR US- 6118719-\$.DID.	USPAT	OR	OFF	2007/03/01 18:08
S41	1	S40 and memory NEAR2 (initializ\$3)	US-PGPUB; USPAT	OR	OFF	2007/03/01 18:09
S5	3	S4 and memory NEAR2 (initializ\$3)	US-PGPUB; USPAT	OR	OFF	2007/03/01 18:09
S42	91	"4,236,207" "6,049,476" "6,092,146" "6,118,719"	US-PGPUB; USPAT	OR	OFF	2007/03/01 18:10
S43	14	S42 and memory NEAR2 (initializ\$3)	US-PGPUB; USPAT	OR	OFF	2007/03/01 18:13
S45	15	S42 and memory near3 test\$3	US-PGPUB; USPAT	OR	OFF	2007/03/01 18:16
S44	3	S42 and memory NEAR2 initializ\$3 near3 test\$3	US-PGPUB; USPAT	OR	OFF	2007/03/01 18:16
S48	46	S46 S47	US-PGPUB; USPAT	OR	OFF	2007/05/17 12:51
S47	35	((PETER) near2 (VOGT)). INV.	US-PGPUB; USPAT	OR	OFF	2007/05/17 12:51
S46	12	((FRANK) near2 (LECLERG)).INV.	US-PGPUB; USPAT	OR	OFF	2007/05/17 12:51

S50	13	"714"/\$.ccls. and (memory NEAR3 access\$3 with initializ\$5 near3 test\$3)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:53
S49	51	(memory NEAR3 access\$3 with initializ\$5 near3 test \$3)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:53
S19	51	(memory NEAR3 access\$3 with initializ\$5 near3 test \$3)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:53
S53	65	"714"/\$.ccls. and (memory NEAR initializ\$5 near3 test \$3)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:54
S52	119	"714"/\$.ccls. and (memory NEAR2 initializ\$5 near3 test \$3)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:54
S51	155	"714"/\$.ccls. and (memory NEAR3 initializ\$5 near3 test \$3)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:54
S54	8	"714"/\$.ccls. and (memory NEAR initializ\$5 near3 test \$3 with module)	US-PGPUB; USPAT	OR	ON	2007/05/17 12:55
S55	17	memory NEAR initializ\$5 near3 test\$3 with module	US-PGPUB; USPAT	OR	ON	2007/05/17 12:56
S56	17	memory NEAR initializ\$5 near3 test\$3 with module AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 12:58
S57	12	buffer and memory NEAR initializ\$5 near3 test\$3 with module AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 12:59
S58	106	buffer with memory with test\$3 with module AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:00
S59	4	buffer adj module with memory with test\$3 AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:01
S61	648	"714"/\$.ccls. and buffer with memory with test\$3 AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:02
S62	35	"714"/\$.ccls. and module with buffer with memory with test\$3 AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:04
S63	347	"714"/\$.ccls. and module with buffer with memory AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:05
S64	23	"714"/\$.ccls. and module adj memory with buffer AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:07

S66	125	"714"/\$.ccls. and memory with initializ\$5 with buffer AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:08
S65	3	"714"/\$.ccls. and module with memory with initializ \$5 with buffer AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:08
S69	56	"714"/\$.ccls. and memory NEAR3 initializ\$5 near3 access\$3 AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:10
S68	3	"714"/\$.ccls. and memory NEAR3 initializ\$5 near3 access\$3 with buffer AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:10
S67	46	"714"/\$.ccls. and memory NEAR3 initializ\$5 with buffer AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:10
S72	5	"714"/\$.ccls. and memory NEAR2 module with initializ \$5 with access\$3 AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:12
S70	25	"714"/\$.ccls. and memory NEAR2 initializ\$5 near2 access\$3 AND @ad<= "20030930"	US-PGPUB; USPAT	OR	ON	2007/05/17 13:12
S73	22	"714"/718-724.ccls. and memory adj initialization	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/05/17 13:23
S1	2	"US 20050071580"	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/05/17 13:23
S74	13	"714"/718-724.ccls. and memory adj initialization AND @ad<= "20030930"	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/05/17 13:31
S75	55	"714"/718-724.ccls. and memory near3 test\$3 near3 initializ\$5 AND @ad<= "20030930"	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/05/17 13:36
S78	15	"714"/718-724.ccls. and memory near3 test\$3 near3 initializ\$5 and buffer AND @ad<= "20030930"	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/05/17 13:37
S77	2	"714"/718-724.ccls. and memory near3 test\$3 near3 initializ\$5 and bus near3 command AND @ad<= "20030930"	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/05/17 13:37

S79	3	(US-7065688-\$ or US-4782486-\$).did. or (US-20050071580-\$).did.	USPAT; DERWENT	OR	OFF	2007/06/07 09:30
S82	2	S79 AND memory ADJ module and initializ\$4	US-PGPUB; USPAT	OR	ON	2007/06/07 09:32
S80	2	S79 AND memory ADJ module	US-PGPUB; USPAT	OR	ON	2007/06/07 09:32
S83	2	S79 AND memory ADJ module and initializ\$5	US-PGPUB; USPAT	OR	ON	2007/06/07 09:36
S84	2	S79 AND memory ADJ module and initializ\$5 and access	US-PGPUB; USPAT	OR	ON	2007/06/07 09:37
S86	2	S79 AND memory ADJ module and initializ\$5 and access\$3 and test\$3	US-PGPUB; USPAT	OR	ON	2007/06/07 09:53
S85	2	S79 AND memory ADJ module and initializ\$5 and access\$3	US-PGPUB; USPAT	OR	ON	2007/06/07 09:53
S88	2	S87 AND memory ADJ module and initializ\$5 and access\$3 and test\$3	US-PGPUB; USPAT	OR	ON	2008/04/18 15:13
S87	3	(US-7065688-\$ or US-4782486-\$).did. or (US-20050071580-\$).did.	USPAT; DERWENT	OR	OFF	2008/04/18 15:13
S91	5	memory ADJ controller WITH initializ\$5 WITH access\$3 WITH test\$3 AND @ad< "20030930"	US-PGPUB; USPAT	OR	ON	2008/04/18 15:15
S90	6	memory ADJ controller WITH initializ\$5 WITH access\$3 WITH test\$3	US-PGPUB; USPAT	OR	ON	2008/04/18 15:15
S92	79	memory ADJ controller WITH initializ\$5 WITH test \$3 AND @ad< "20030930"	US-PGPUB; USPAT	OR	ON	2008/04/18 15:17
S93	9	memory ADJ controller WITH initializ\$5 WITH test \$3 with module AND @ad< "20030930"	US-PGPUB; USPAT	OR	ON	2008/04/18 15:19
S94	5	memory ADJ controller WITH initializ\$5 WITH test \$3 with access\$3 AND @ad< "20030930"	US-PGPUB; USPAT	OR	ON	2008/04/18 15:38
S96	429	memory near3 (controller same processor same module)	USPAT	OR	OFF	2008/08/21 18:13
S95	72737	memory near3 (controller samne processor same module)	USPAT	OR	OFF	2008/08/21 18:13

S97	322	memory near3 (controller same processor same module) AND @ad< "20030930"	USPAT	OR	OFF	2008/08/21 18:14
S98	9	memory near3 (controller same processor same module) and initializ\$5 near3 test\$3 AND @ad< "20030930"	USPAT	OR	OFF	2008/08/21 18:16
S99	35	memory adj module near3 (initializ\$5 acess\$3) and (controller same processor) AND @ad< "20030930"	USPAT	OR	OFF	2008/08/21 18:21
S100	32	memory adj module near2 (initializ\$5 acess\$3) and (controller same processor) AND @ad< "20030930"	USPAT	OR	OFF	2008/08/21 18:22
S103	22	test\$3 and memory adj module near2 (initializ\$5 acess\$3) and (controller same processor) AND @ad< "20030930"	USPAT	OR	OFF	2008/08/21 18:30
S104	22	buffer and memory adj module near2 (initializ\$5 acess\$3) and (controller same processor) AND @ad< "20030930"	USPAT	OR	OFF	2008/08/21 18:33

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